



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ken Jaramillo

Art Unit: 2112

Serial No.: 10/039,707

Examiner: Christopher E. Lee

Filed: 11-9-01

Docket: 101950.00076 – OLD #
125273.00045 – NEW #

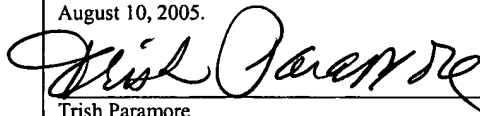
For: **METHOD FOR INCREASING PERIPHERAL COMPONENT INTERCONNECT (PCI) BUS THROUGHPUT VIA A BRIDGE FOR MEMORY READ TRANSFERS VIA DYNAMIC VARIABLE PREFETCH**

PRELIMINARY AMENDMENT

August 10, 2005

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. 1.8(A)
I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on August 10, 2005.


Trish Paramore

Sir:

Prior to the examination please amend the above identified application as follows: